

DOCKET NO.: P99-16

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Inventor: Robert C. Newman, Jr.
Serial No.: 08/887,848
Filed: June 22, 2001
Title: ELECTRONIC BALLAST
Art Unit: 2821
Examiner: Lee, Wilson

Date of Deposit: November 23, 2005
I hereby certify that this paper or fee is being deposited with the United States Postal Service as First Class Mail on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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- In accordance with § 1.97(b), since this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, within three months of the date of entry into the national stage of the above identified application as set forth in § 1.491, before the mailing date of a first Office Action on the merits of the above-identified application, or before the mailing date of a first Office Action after the filing of a Request for Continued Examination (RCE) under § 1.114, no additional fee is required.
- In accordance with § 1.97(c), this Information Disclosure Statement is being filed after the period set forth in § 1.97(b) above but before the mailing date of either a Final Action under § 1.113, a Notice of Allowance under § 1.311, or an action that otherwise closes prosecution in the application, therefore:
 - Certification in Accordance with § 1.97(e) is attached; or
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- Copies of each of the references listed on the attached Form PTO/SB/08 (or PTO-1449) are enclosed herewith.
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 - In accordance with § 1.98(d), copies of the following references listed on the attached Form PTO/SB/08 (or PTO-1449) are not enclosed herewith because they were previously cited by or submitted to the U.S. Patent and Trademark Office in patent application(s) for which a claim for priority under 35 U.S.C. § 120 have been made in the instant application: [list as appropriate].
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 - If any of the foregoing publications are not available to the Examiner, Applicant will endeavor to supply copies at the Examiner's request.

There are no listed references that are not in the English language.

Please charge any deficiency or credit any overpayment to Deposit Account No. 12-2421.

This form is submitted in triplicate.

Date: 11/23/2005

Mark E. Rose

Signature

Mark E. Rose

Registration No. 42,113

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PTO/SB/08A (07-05)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

of 2

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Application Number	09/887,848
Filing Date	June 22, 2001
First Named Inventor	Robert C. Newman, Jr.
Art Unit	2821
Examiner Name	Lee, Wilson
Attorney Docket Number	P99-16

U. S. PATENT DOCUMENTS

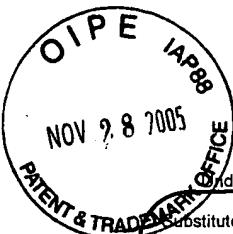
FOREIGN PATENT DOCUMENTS

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Attorney Docket Number	P99-16

Sheet	2	of	2	Attorney Docket Number	P99-16
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		KHERALUWALA, M. H., et al., "Modified valley fill high power factor electronic ballast for compact fluorescent lamps", Power Electronics Specialists Conf., 1995. PESC'95 Record, 26th Annual IEEE Atlanta, GA USA, 18-22 June 1995, New York, NY, USA, IEEE US 18, June 1995, pp. 10-14, ISBN: 0-7803-2730-6	✓
		PEPHANY, S., "MOSFET Switch Provides Efficient AC/DC Conversion", EDN Electrical Design News, Cahners Publishing Co., Newtown, MA, USA, vol. 45, no. 2, 17 February 2000, p. 149, ISSN: 0012-7515	✓
		ALONSO, J.M., et al., "Evaluation of a Novel Single-Stage High-Power-Factor Electronic Ballast Based on Integrated Buck Half-Bridge Resonant Inverter", Applied Power Electronics Conf. & Exposition, 2000. APEC 2000. 15th Annual IEEE, New Orleans, LA, USA, 6-10 Feb. 2000, Piscataway, NJ, USA, IEEE, US, 6 Feb. 2000, pp. 610-616, ISBN: 0-7803-5864-3	✓
		RIBAS, J., et al., "A New Discharge Lamp Ballast Based on a Self-Oscillating Full-Bridge Inverter Integrated with a Buck-Type PFC Circuit", APEC 2001. 16th Annual IEEE Applied Power Electronics Conf. & Exposition, Anaheim, CA, 4-8 March 2001, Annual Applied Power Electronics Conf., New York, NY: IEEE, US vol. 2 of 2. Conf. 16, 4 March 2001, pp. 688 - 694, ISBN: 0-7803-6618-2	✓

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